

FIG. 1

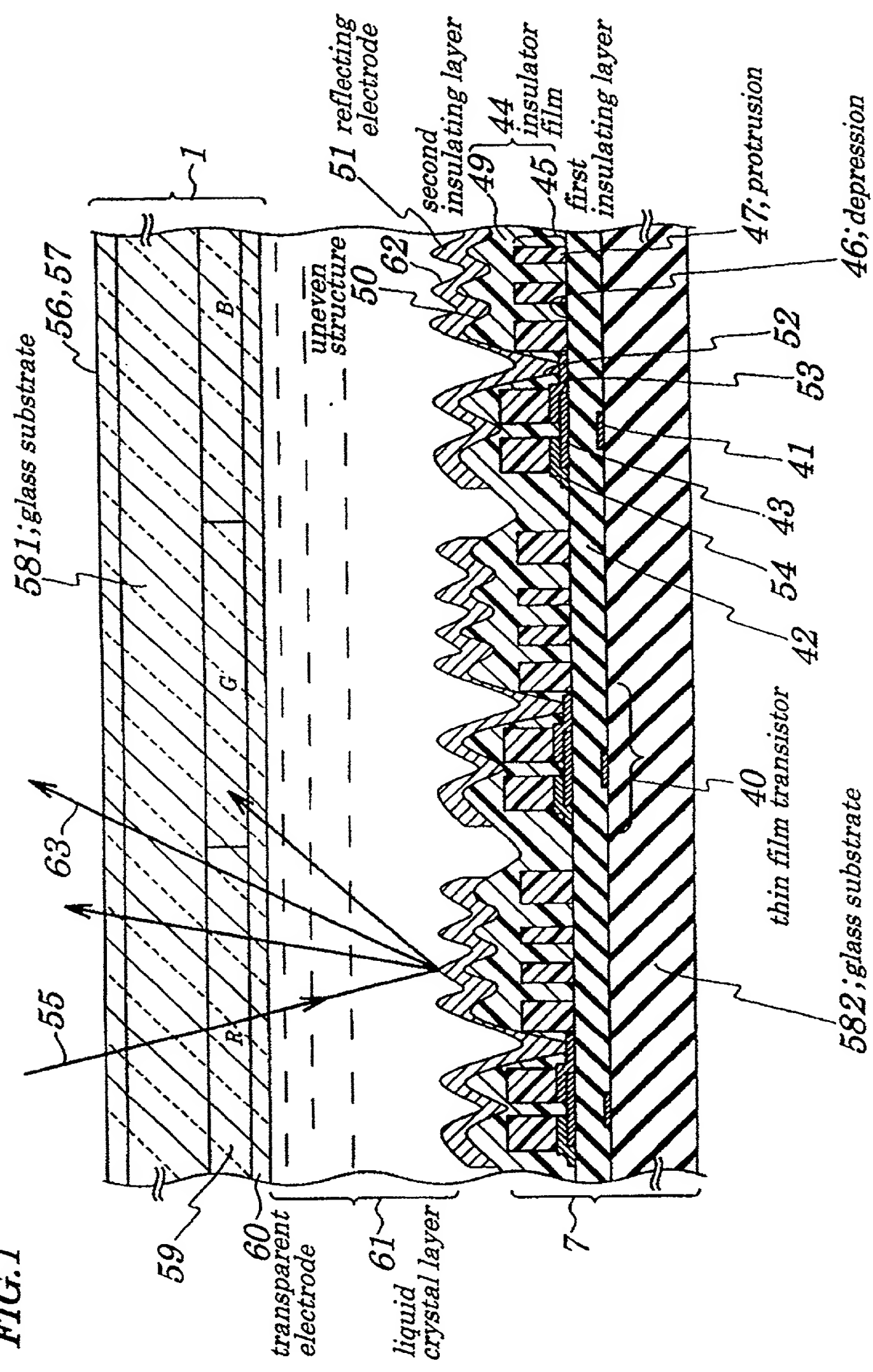


FIG.2A

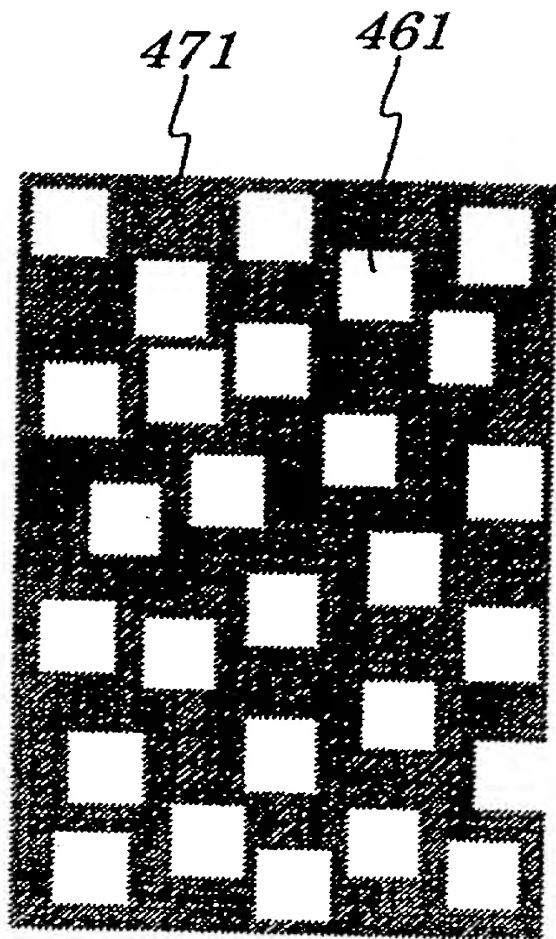


FIG.2B

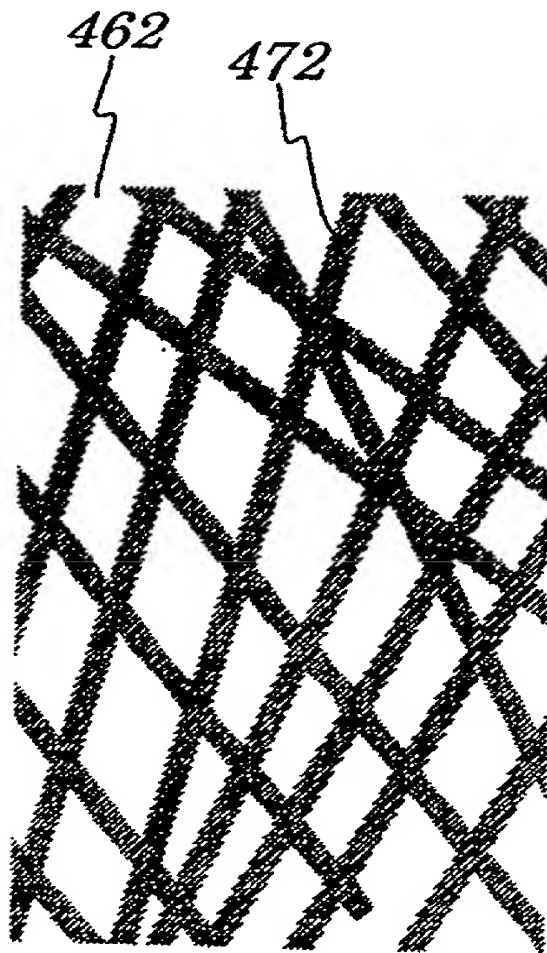


FIG.3A

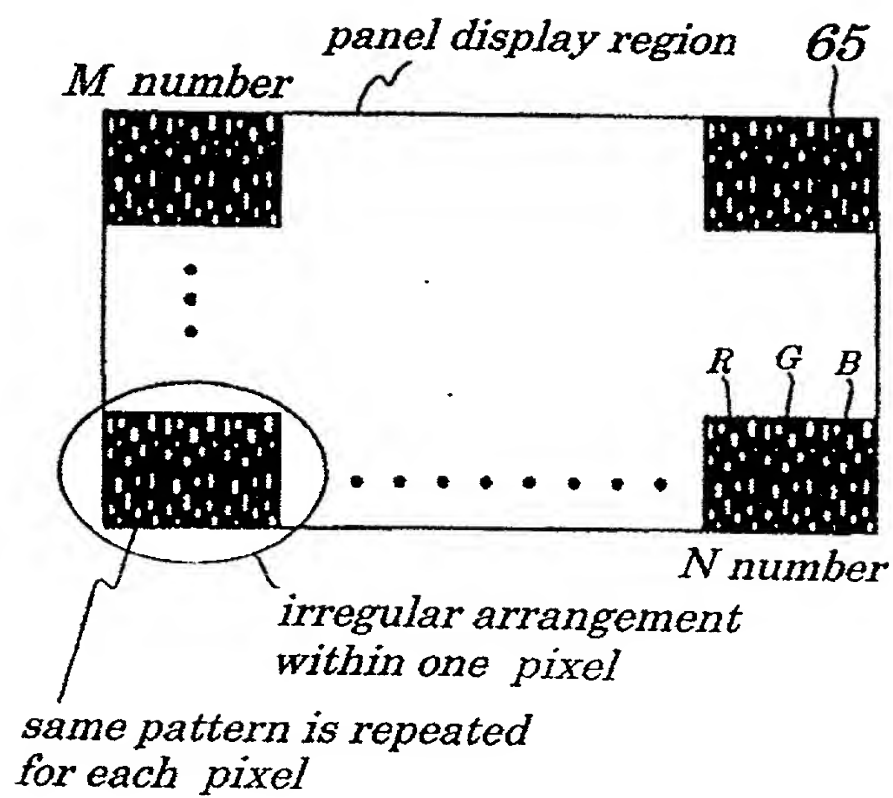


FIG.3B

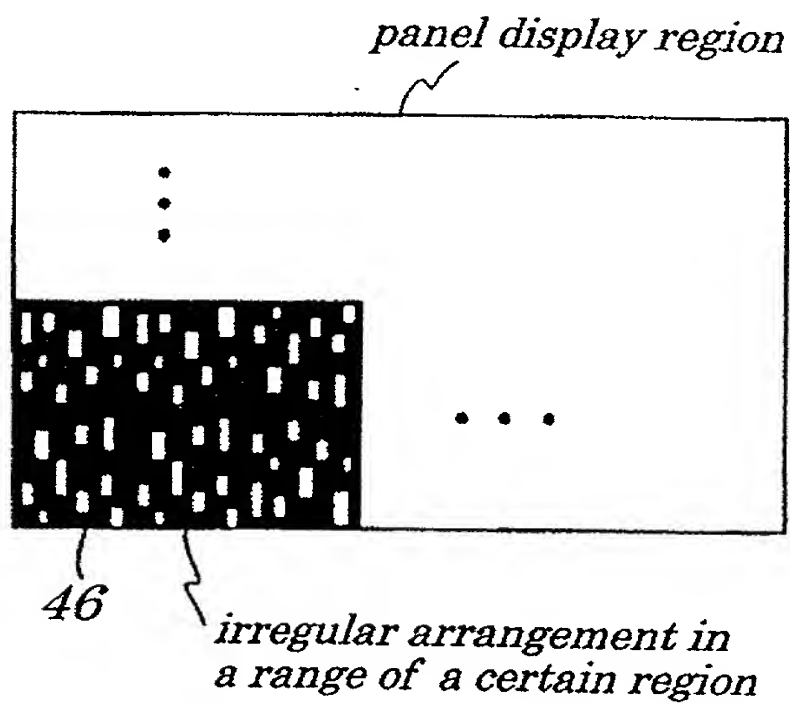


FIG. 4A

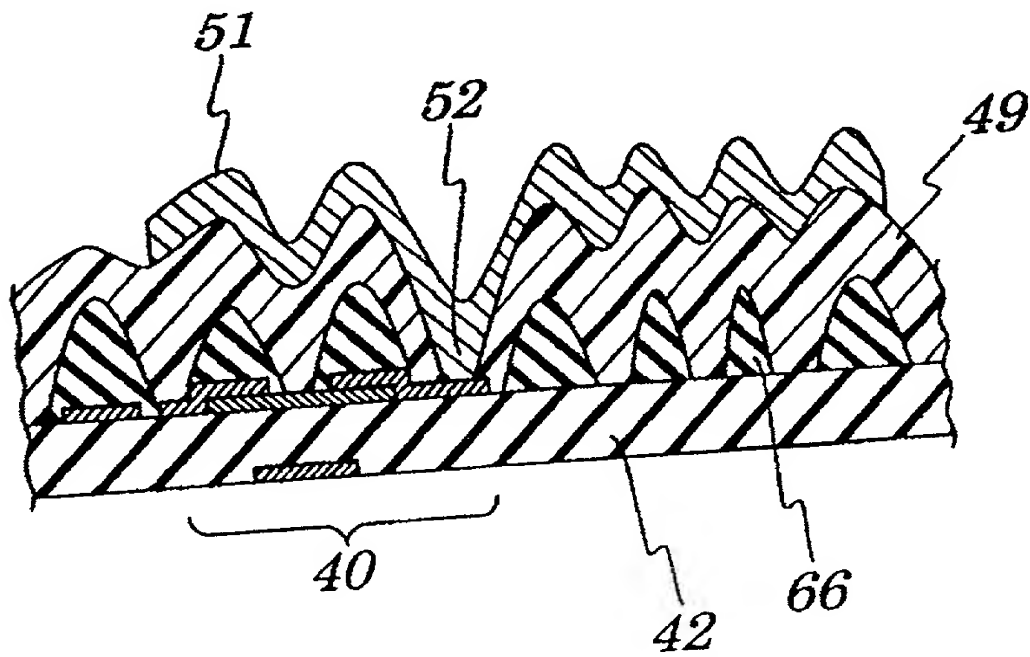


FIG. 4B

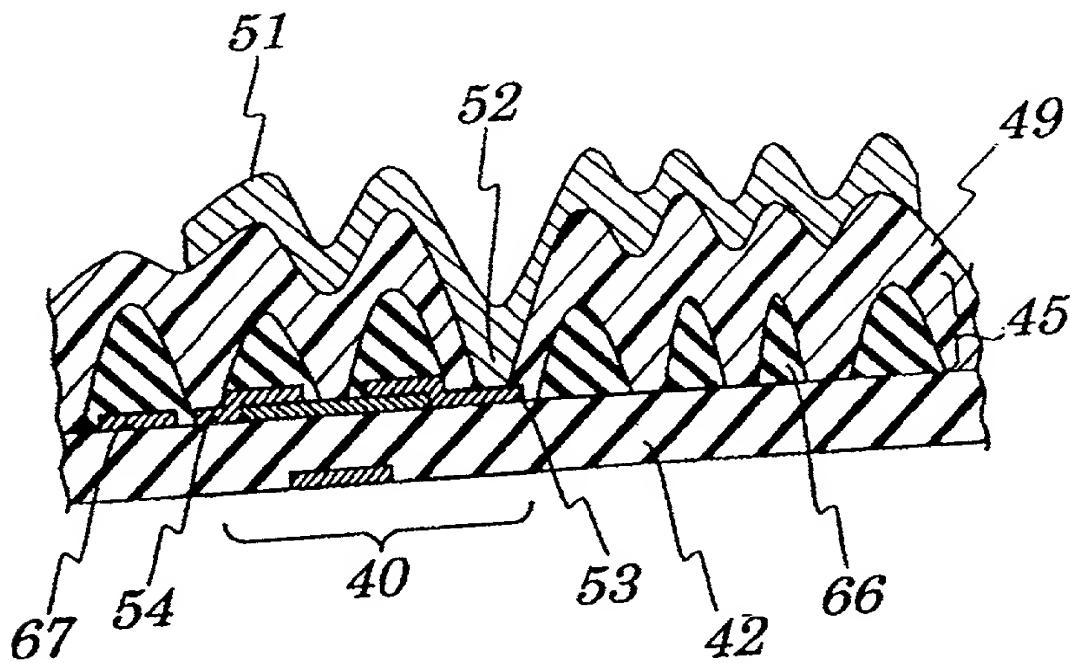


FIG.5A

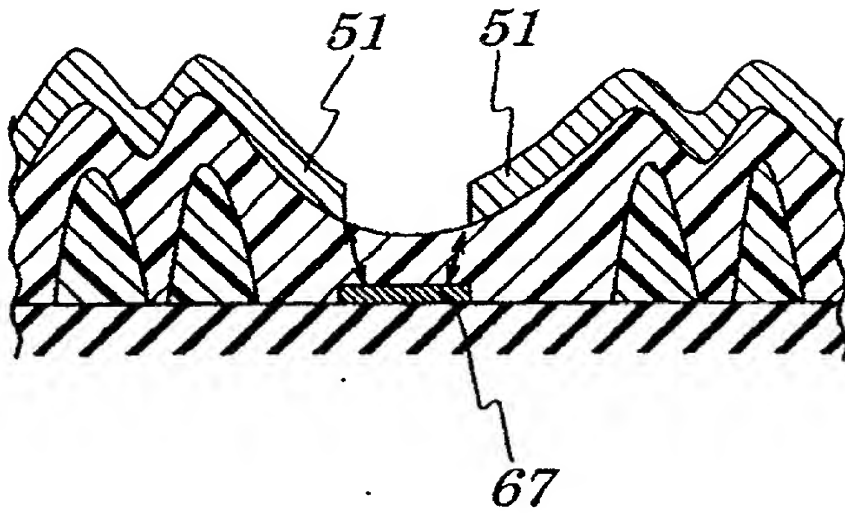


FIG.5B

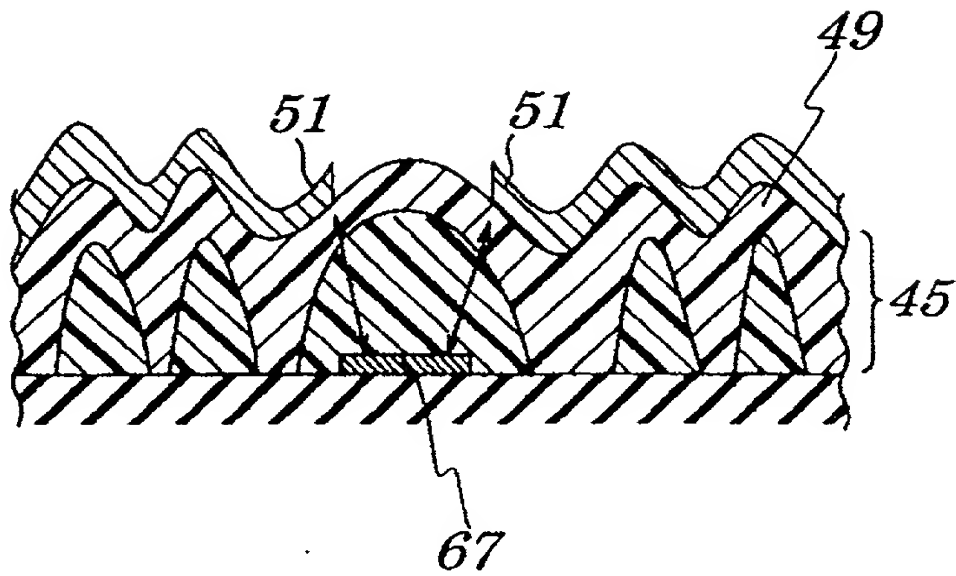
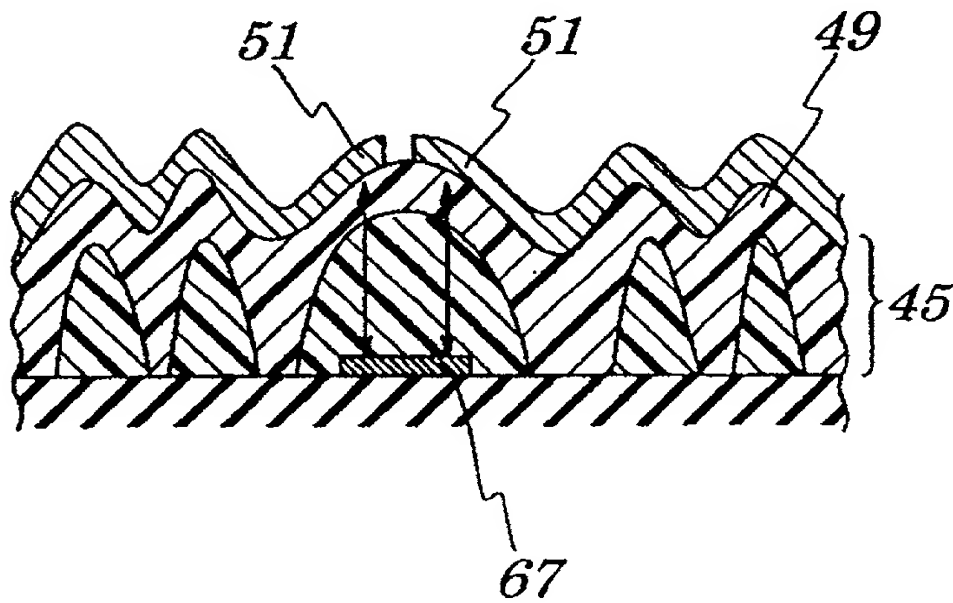


FIG.5C



A cross-sectional view of a multi-layered structure. The top surface is wavy, with peaks labeled 81 and valleys labeled 52. The structure consists of several layers: a top layer with diagonal hatching (51), a middle layer with vertical hatching (53), and a bottom layer with horizontal hatching (42). A bracket labeled 40 indicates a specific region within the structure. Other labels include 80, 81, 45, 67, 54, 53, 42, and 66.

FIG. 7AB

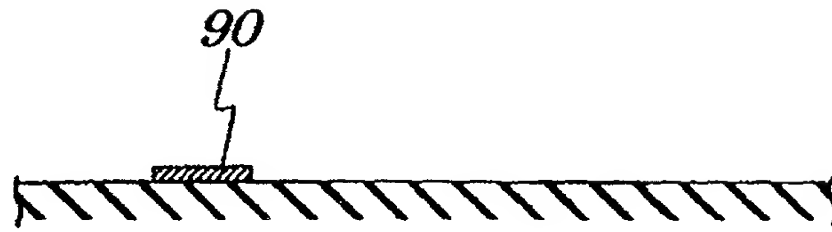


FIG. 7C

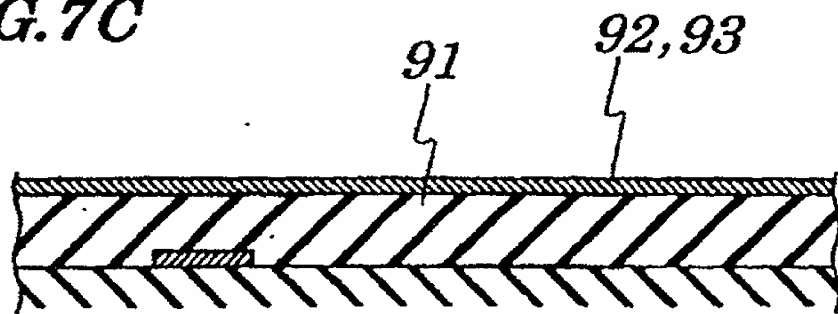


FIG. 7D

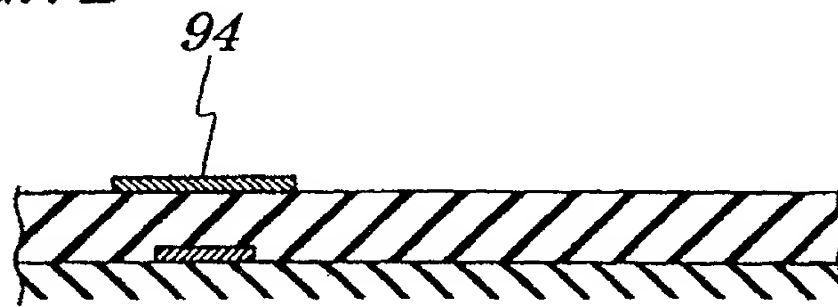


FIG. 7EF

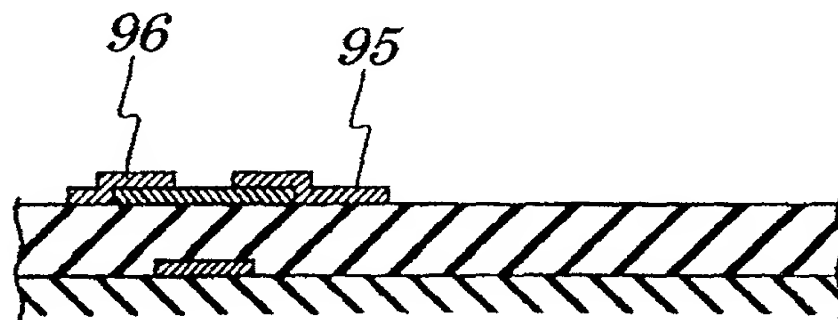


FIG. 7G

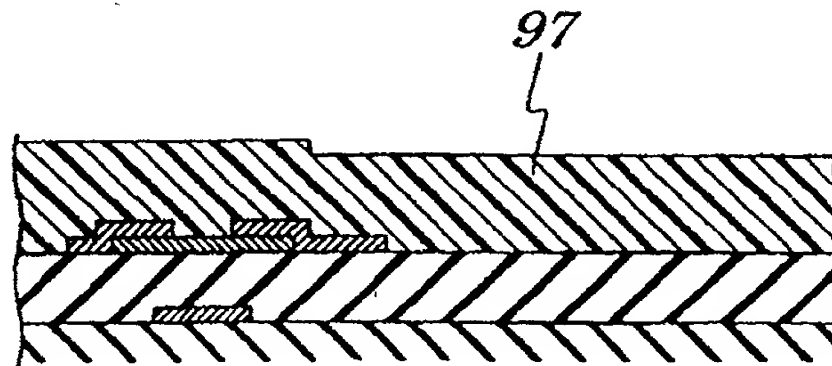


FIG. 8H

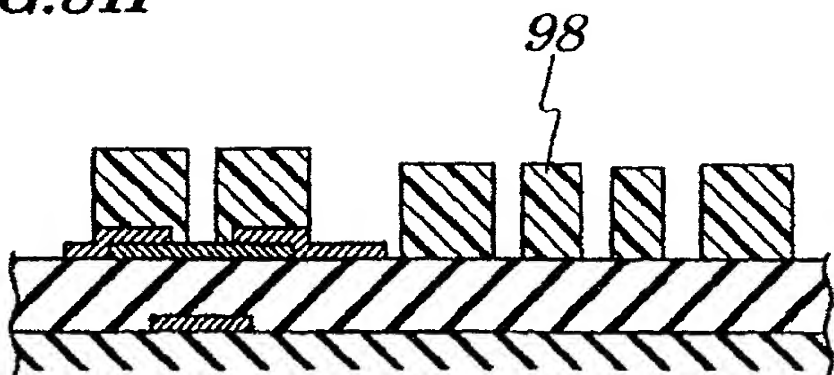


FIG. 8I

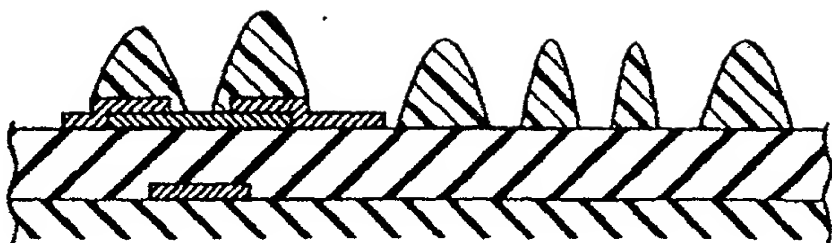


FIG. 8JK

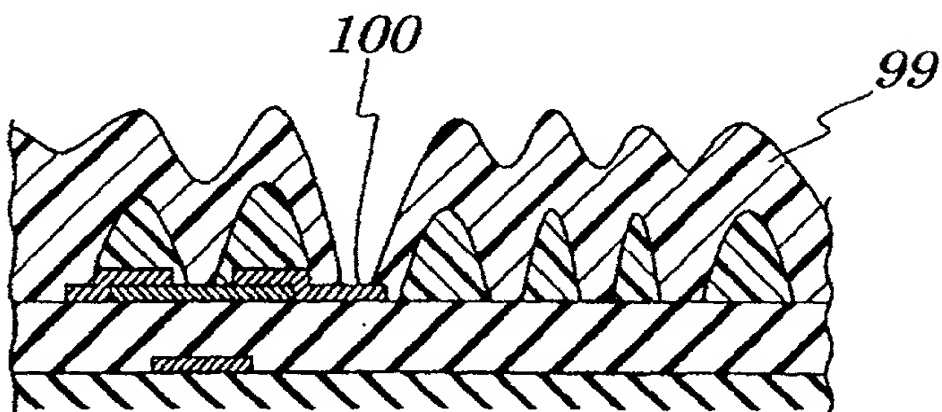


FIG. 8L

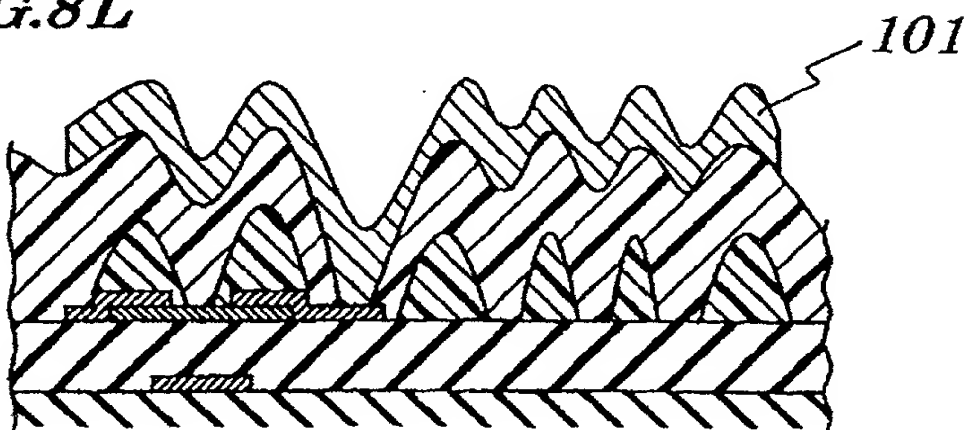


FIG. 9AB

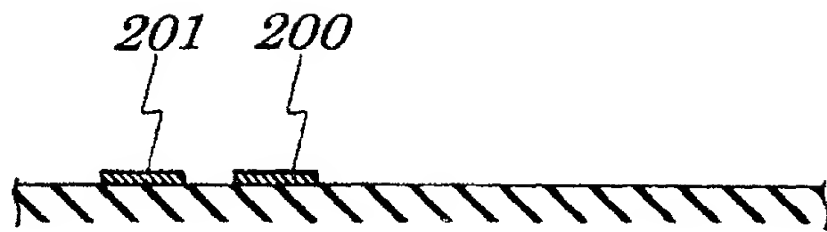


FIG. 9C

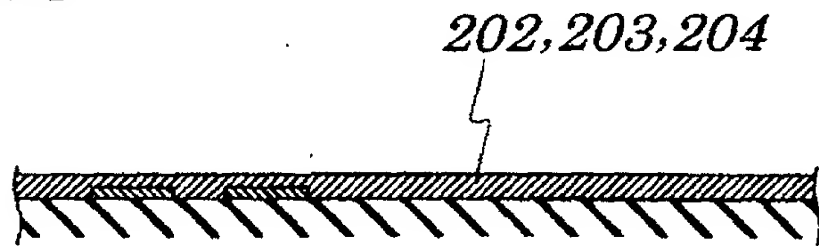


FIG. 9DE

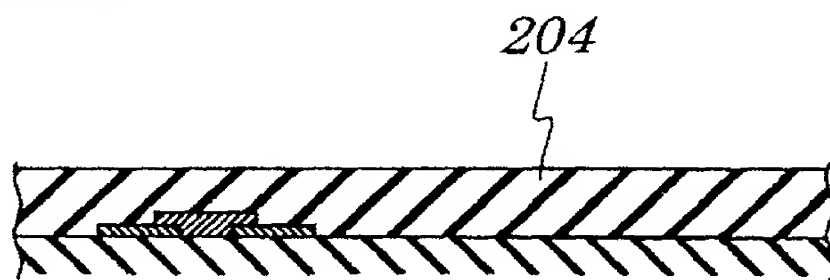


FIG. 9FG

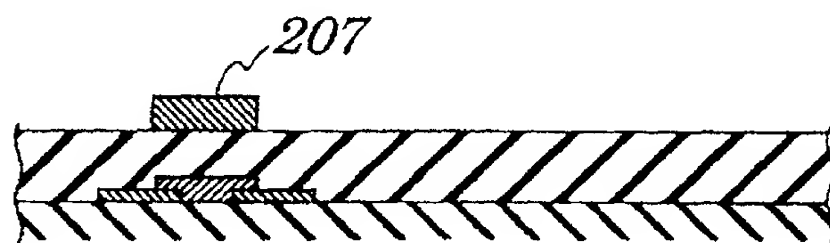


FIG. 9H

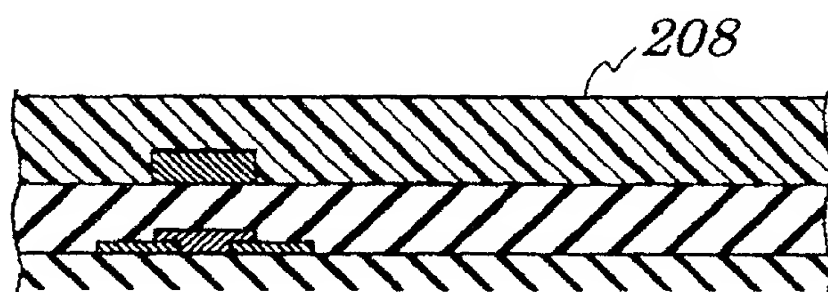


FIG.10I

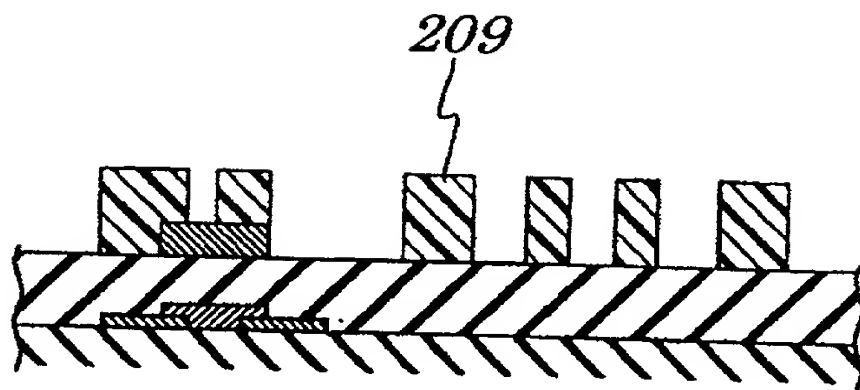


FIG.10JK

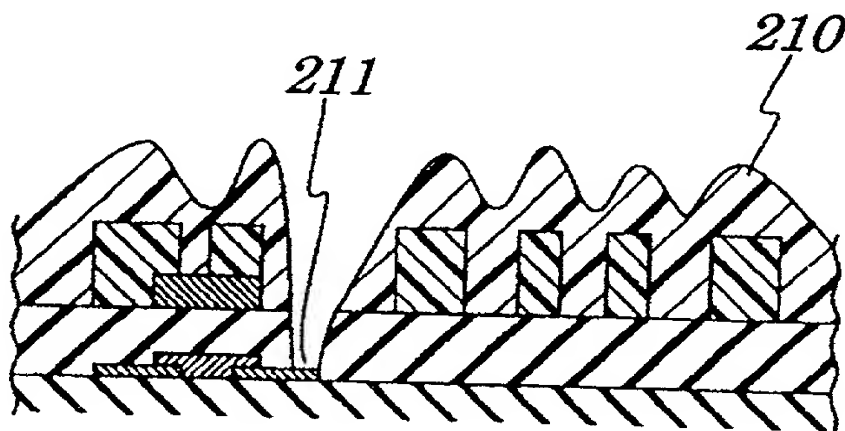


FIG.10LM

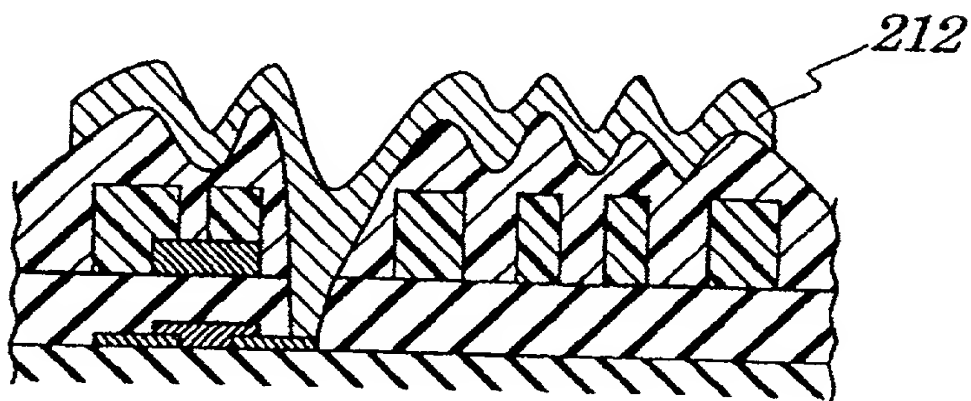


FIG. 11

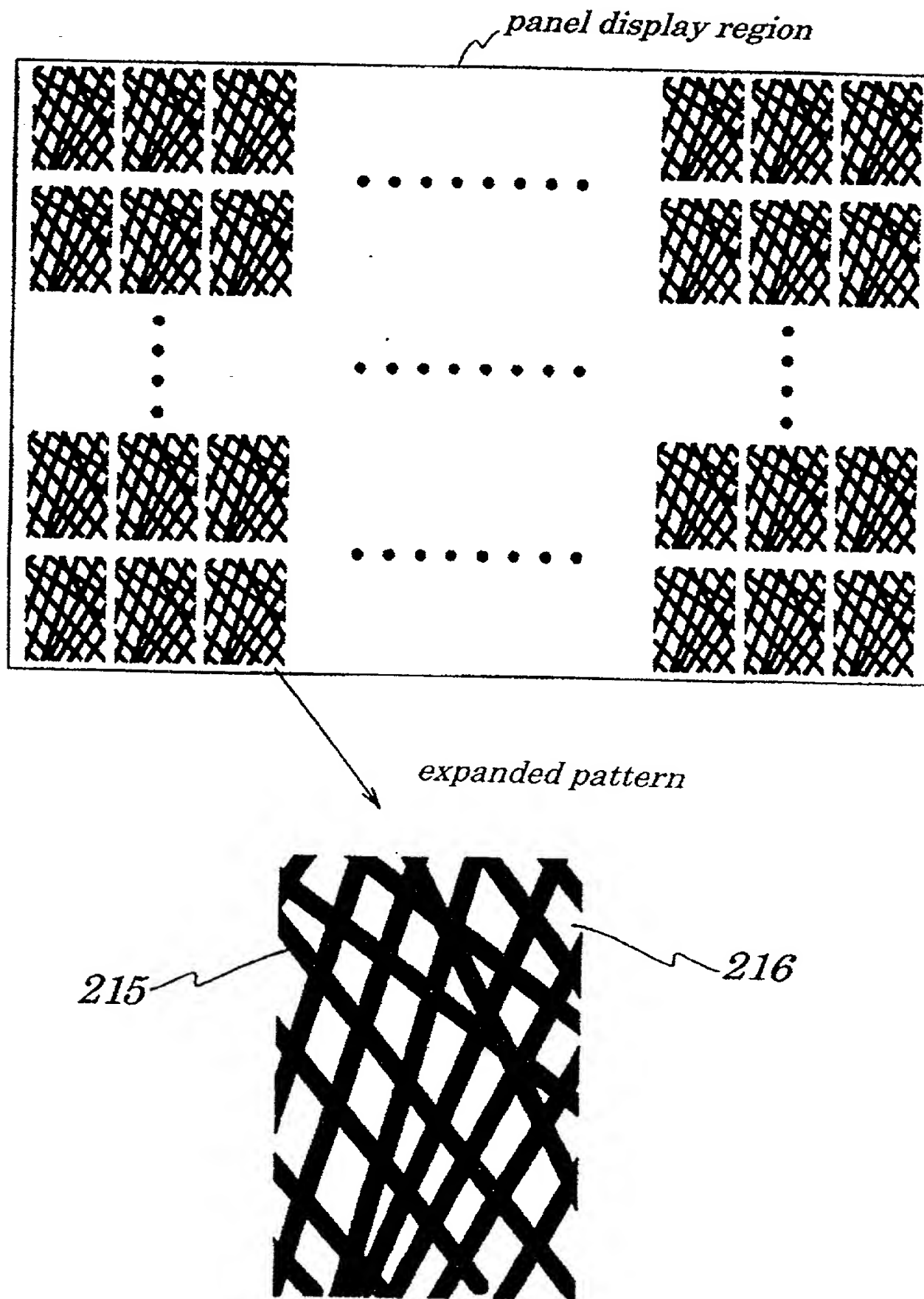


FIG.12

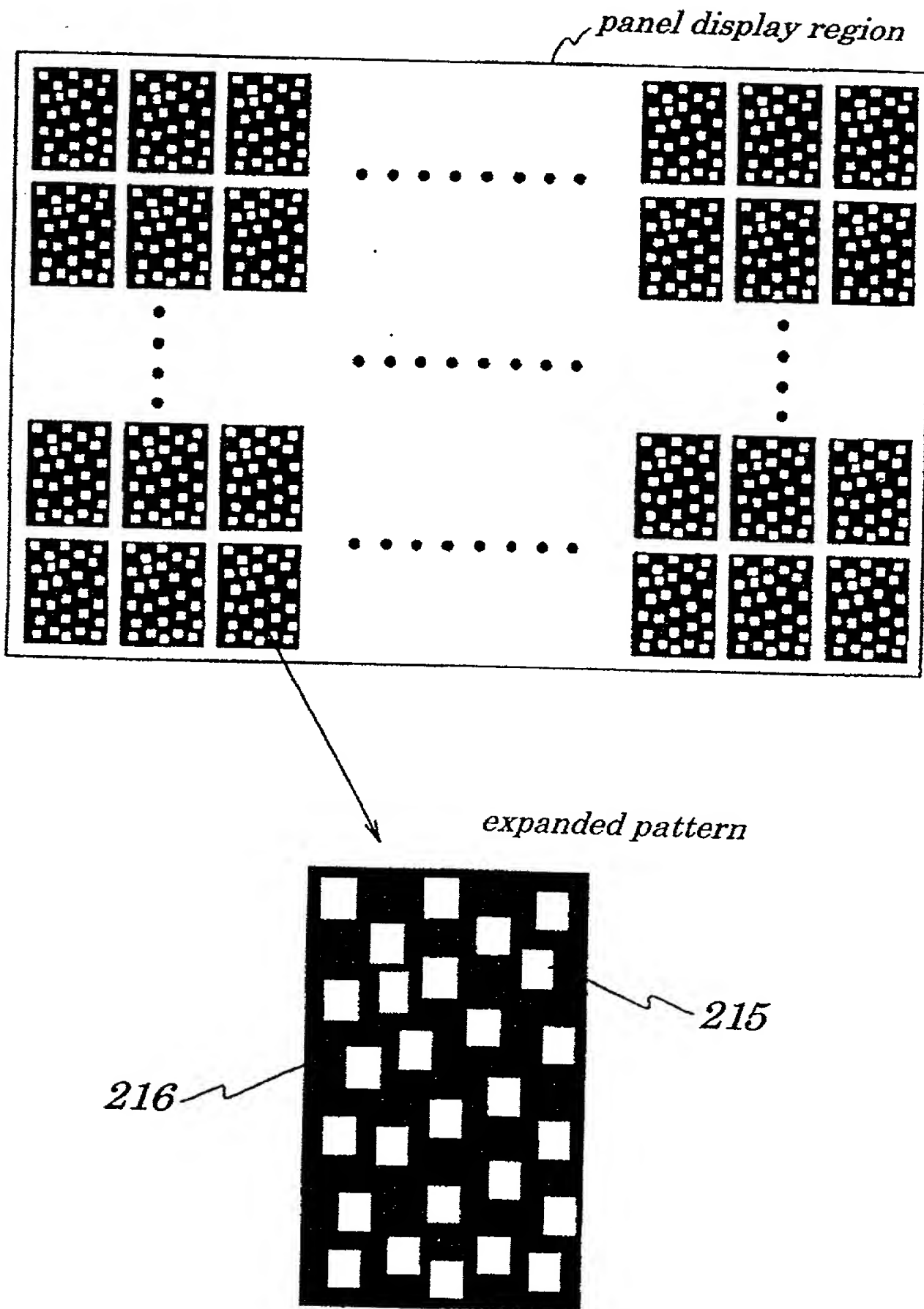


FIG.13AB



FIG.13C

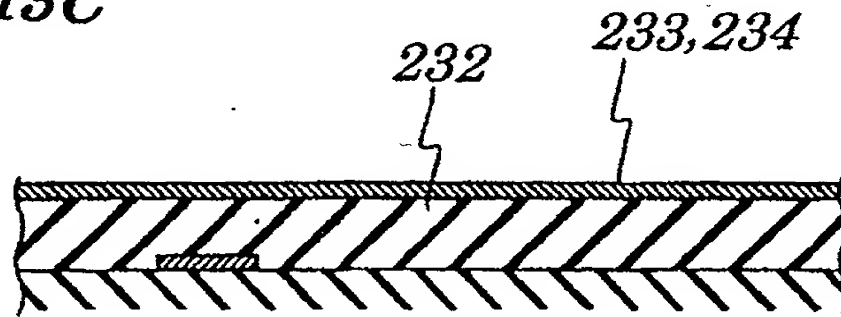


FIG.13D

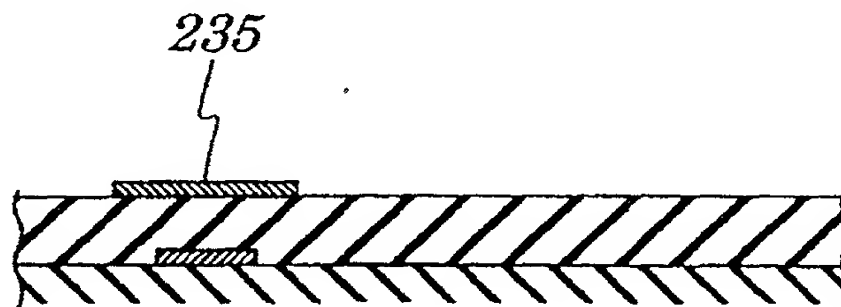


FIG.13EF

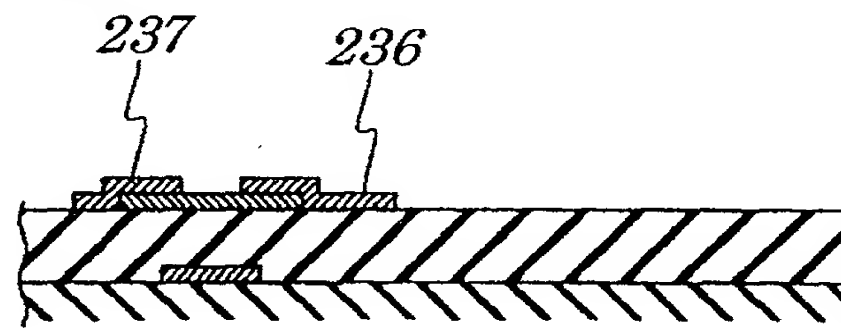


FIG.13G

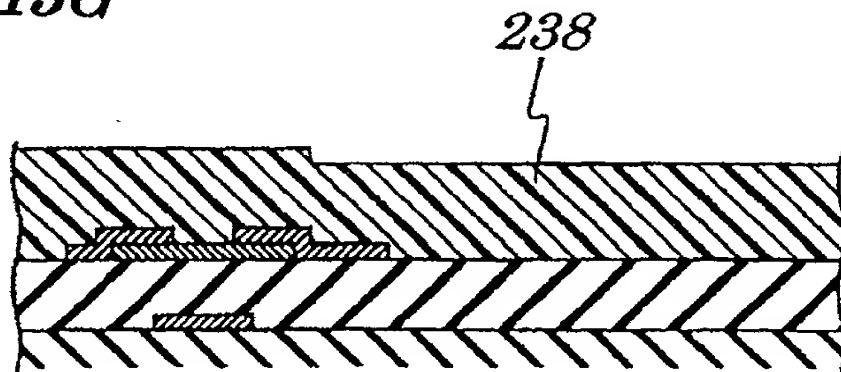


FIG.14H

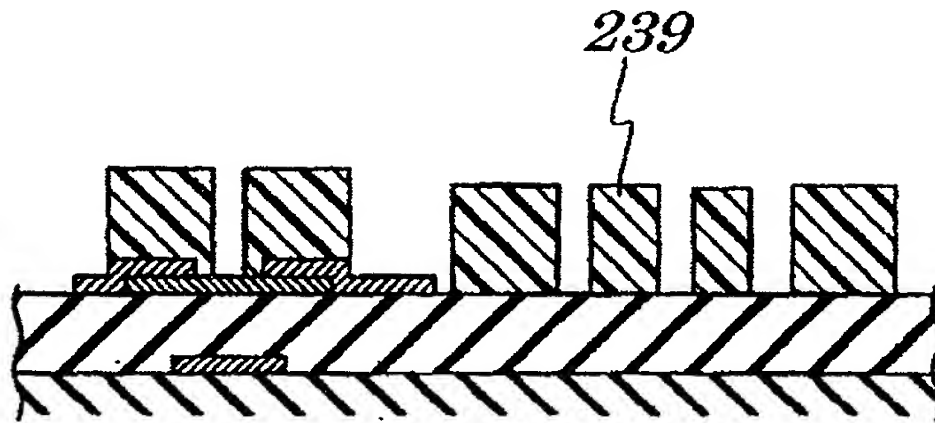


FIG.14IJ

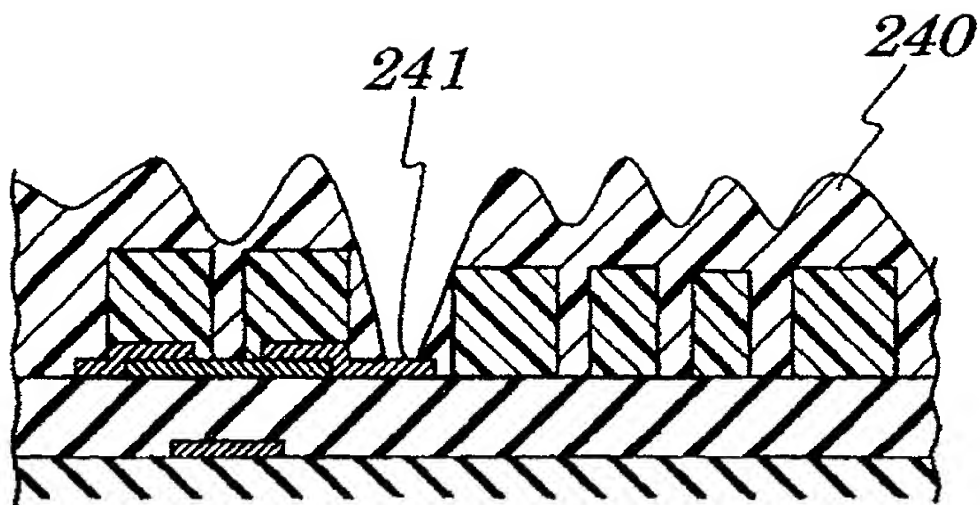


FIG.14KL

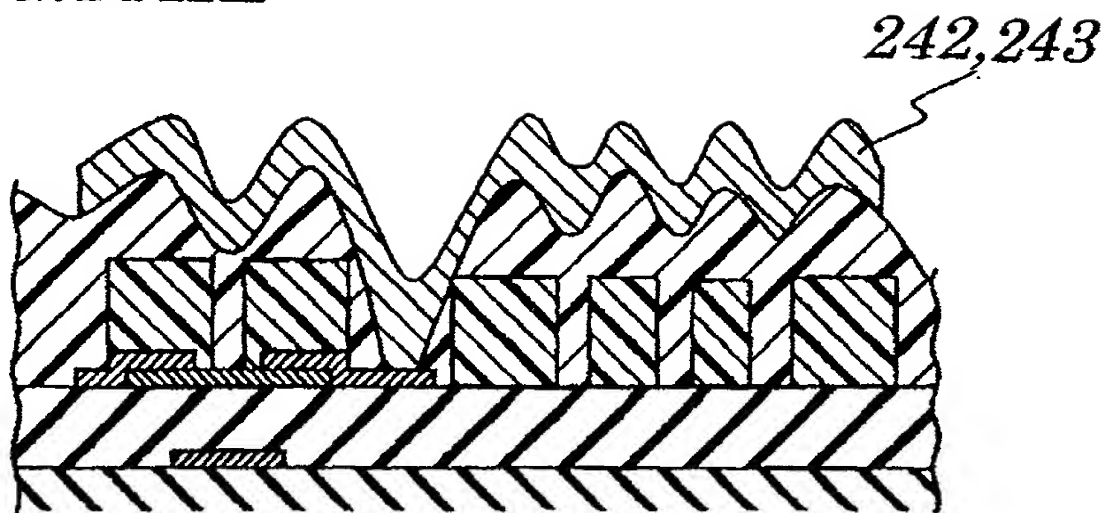


FIG.15AB

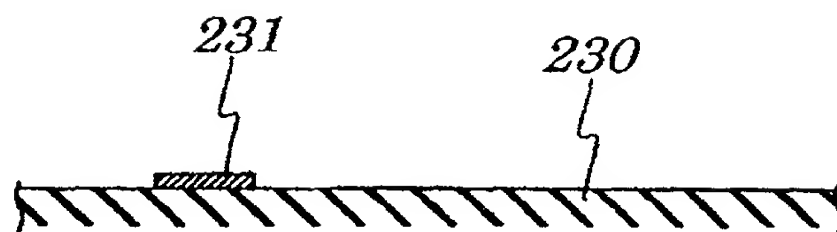


FIG.15C

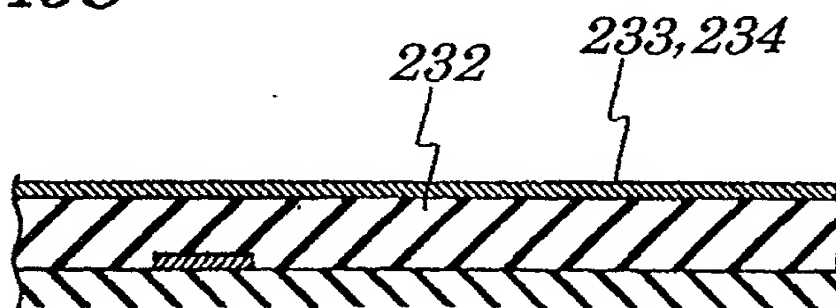


FIG.15D

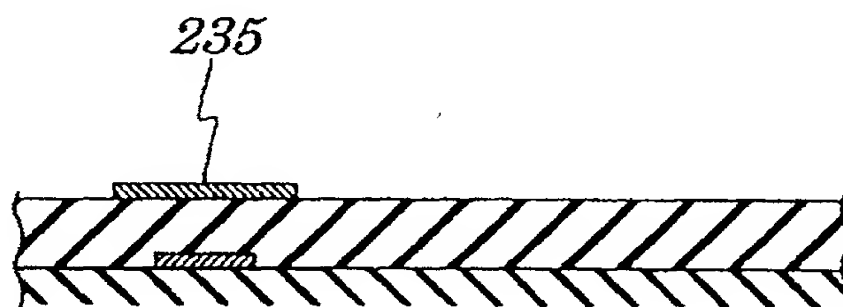


FIG.15EF

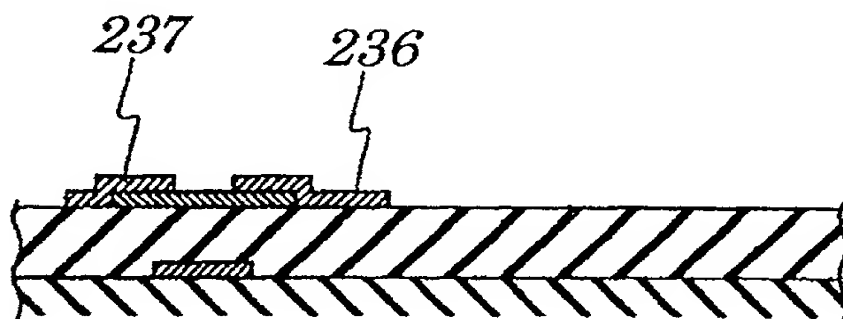


FIG.15G

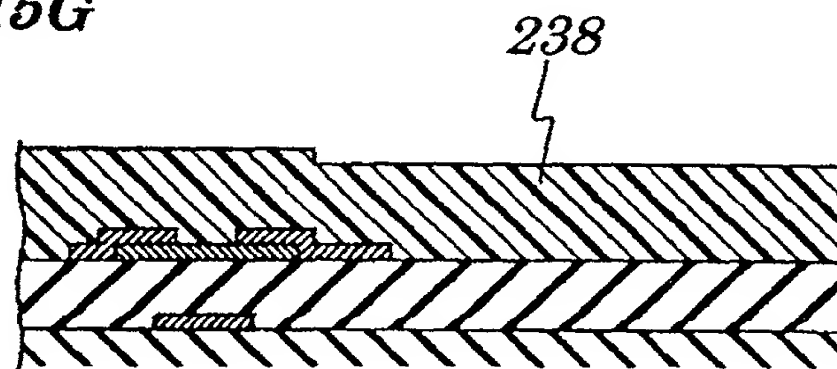


FIG.16H

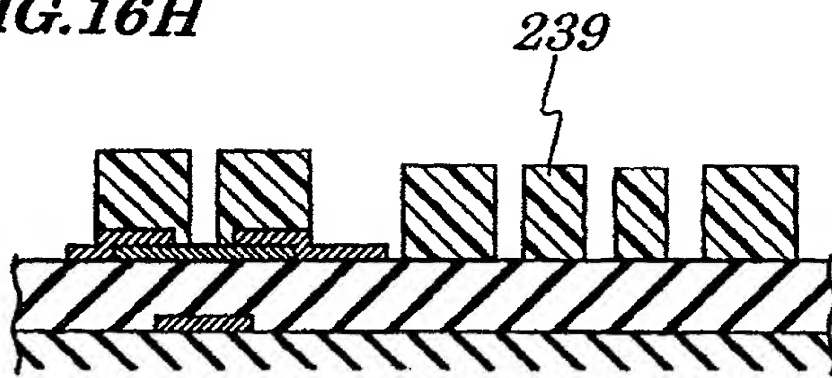


FIG.16I

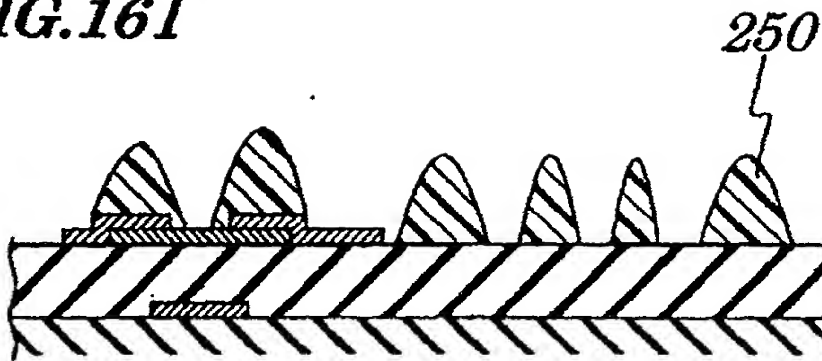


FIG.16JK

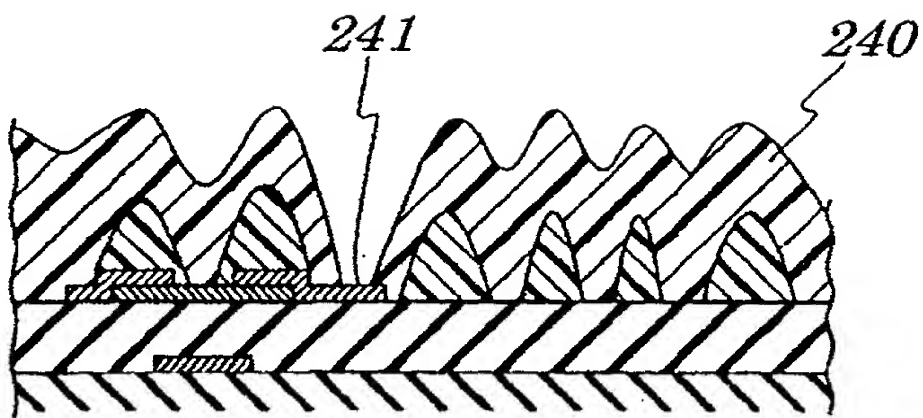


FIG.16L

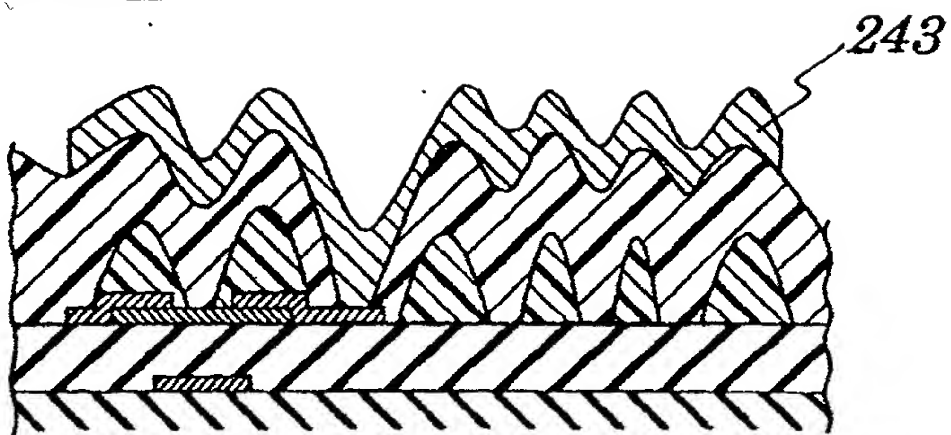


FIG.17

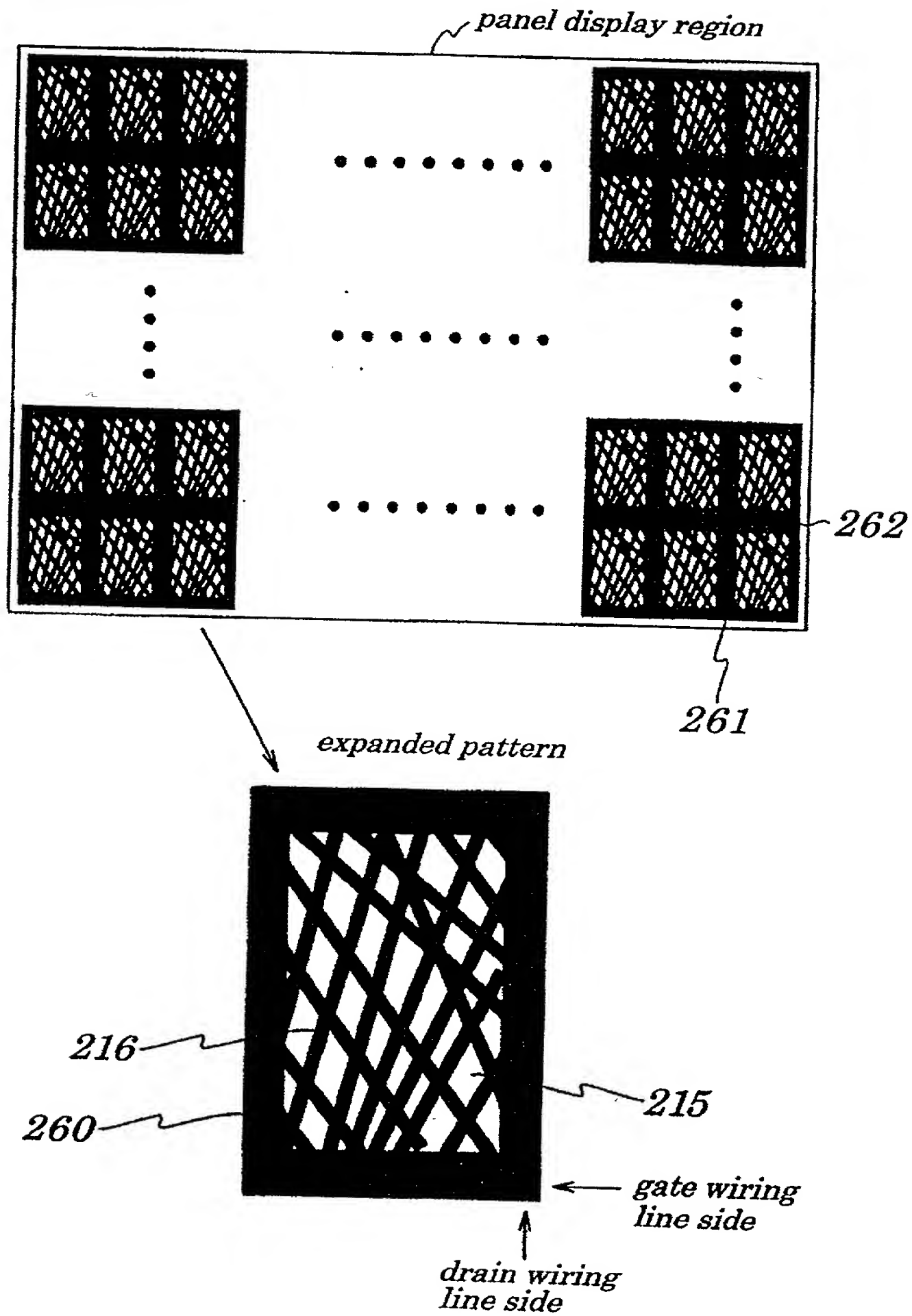


FIG. 18

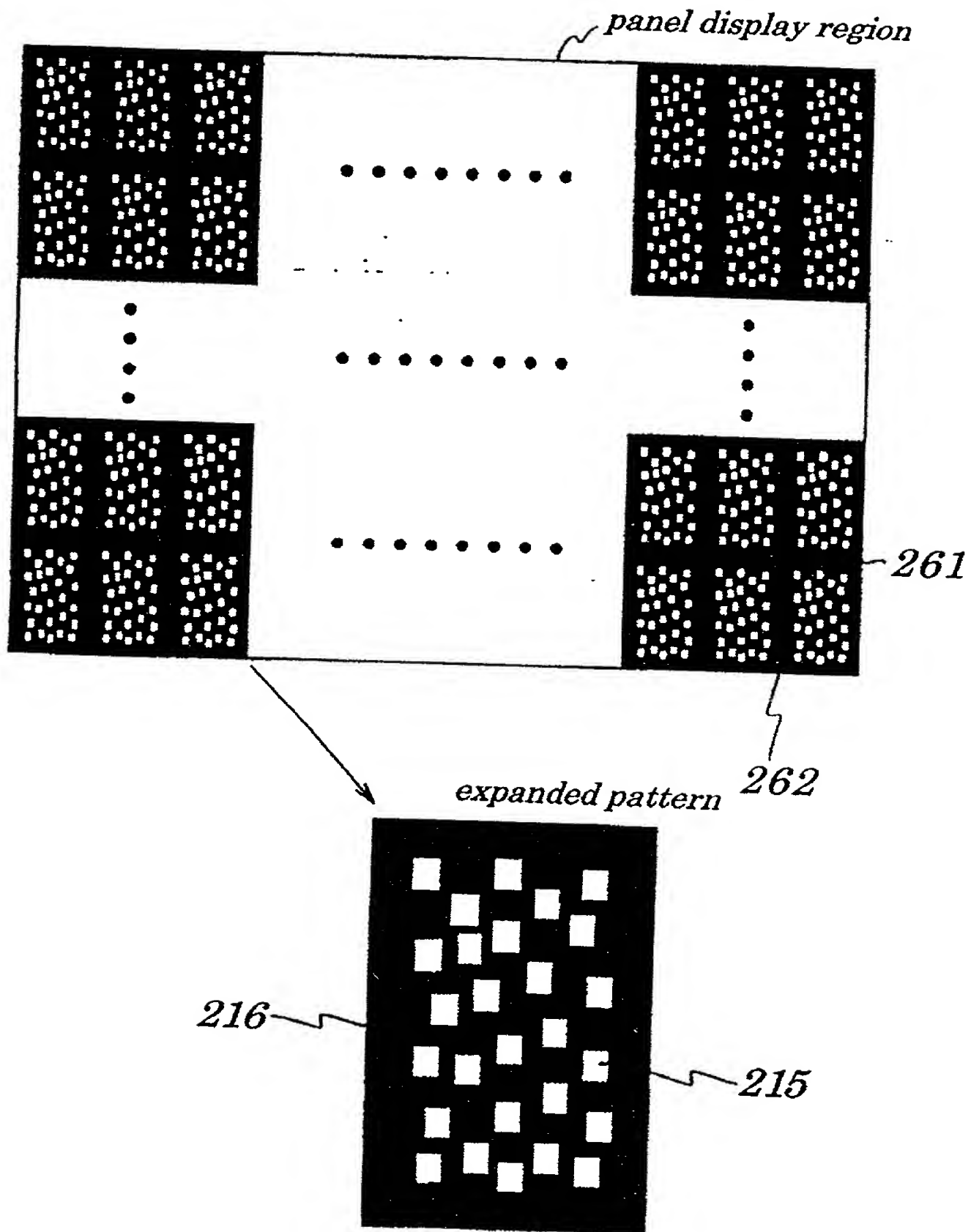


FIG.19 (PRIOR ART)

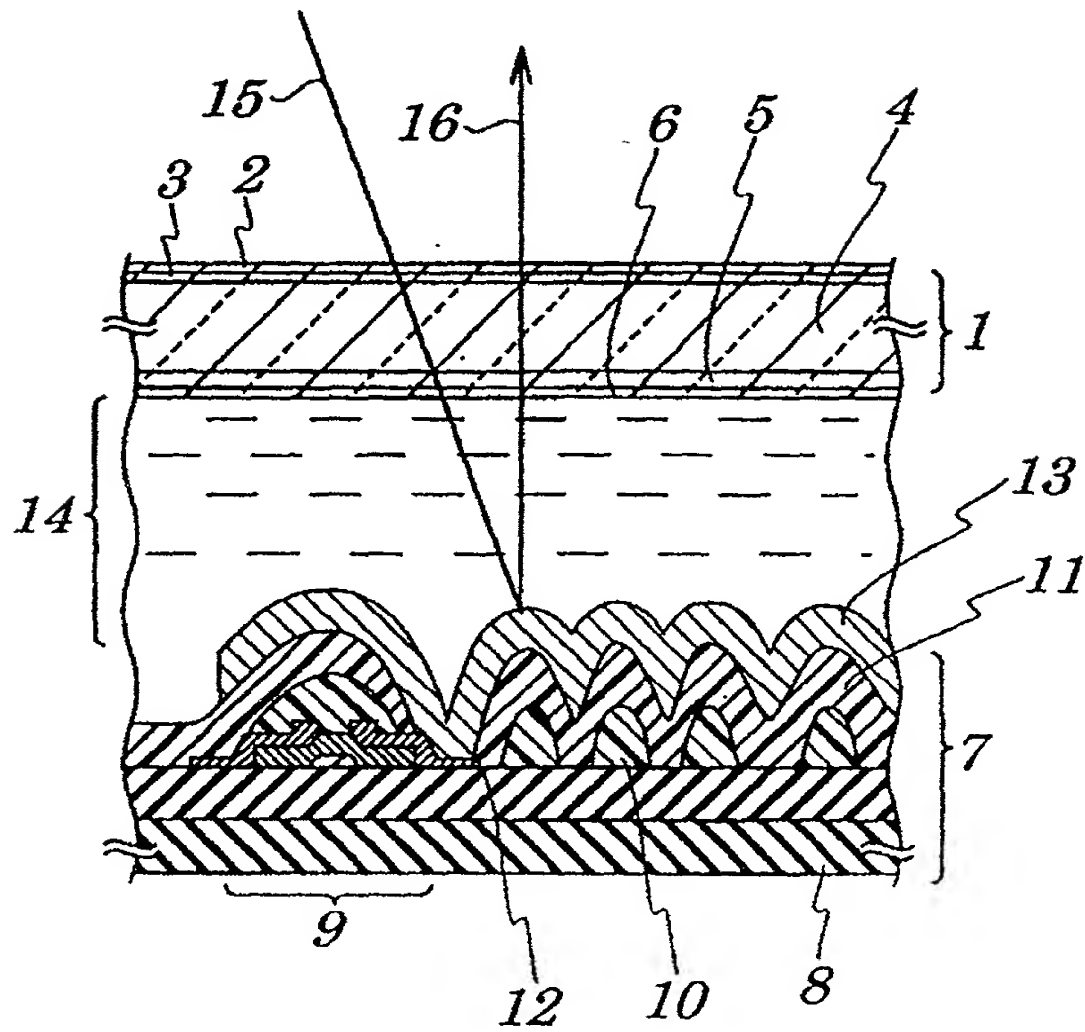


FIG.20A (PRIOR ART)

forming gate
electrode

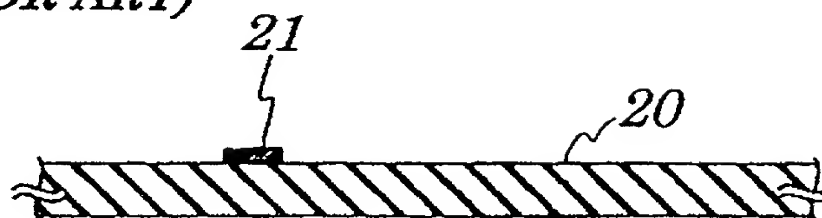


FIG.20B (PRIOR ART)

forming insulator film,
semiconductor film

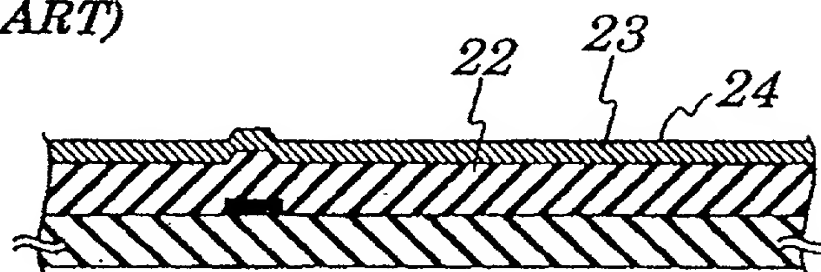


FIG.20C (PRIOR ART)

forming semiconductor
film pattern

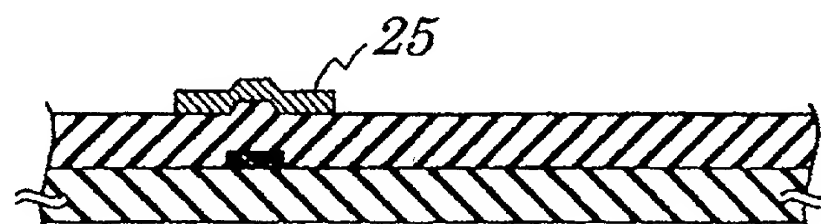


FIG.20D (PRIOR ART)

forming source electrode,
drain electrode

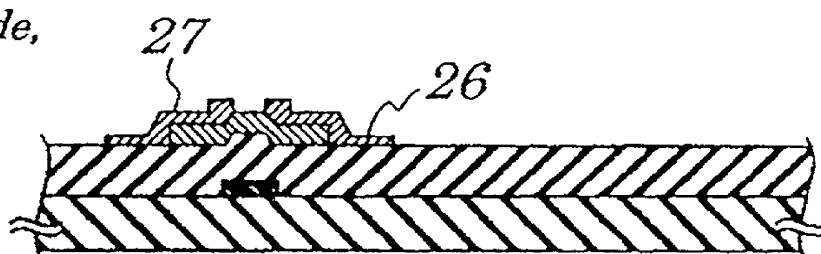


FIG.20E (PRIOR ART)

forming organic
insulating layer

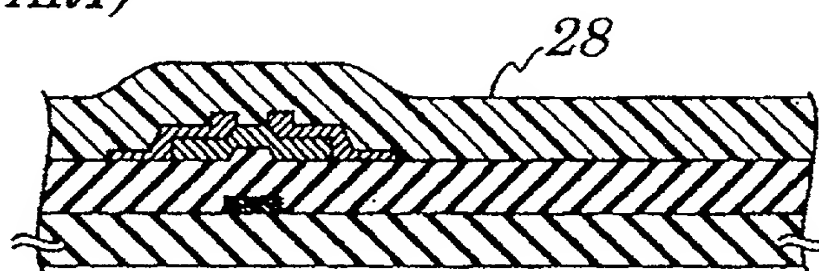


FIG.20F (PRIOR ART)

forming protrusion shape

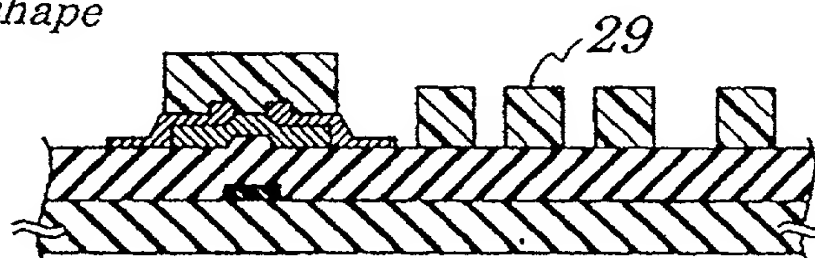


FIG.21G (PRIOR ART)
smooth protrusion shape

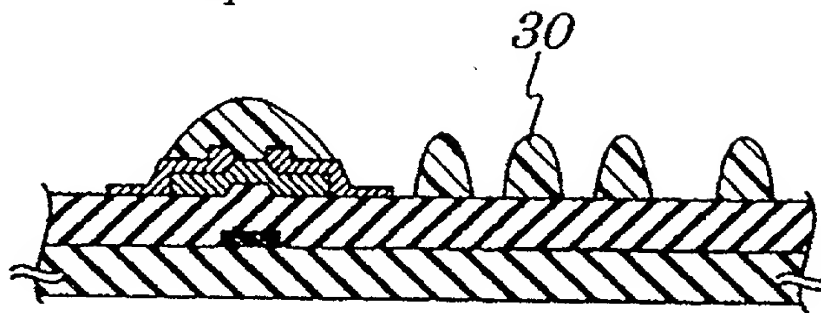


FIG.21H (PRIOR ART)
forming insulator film

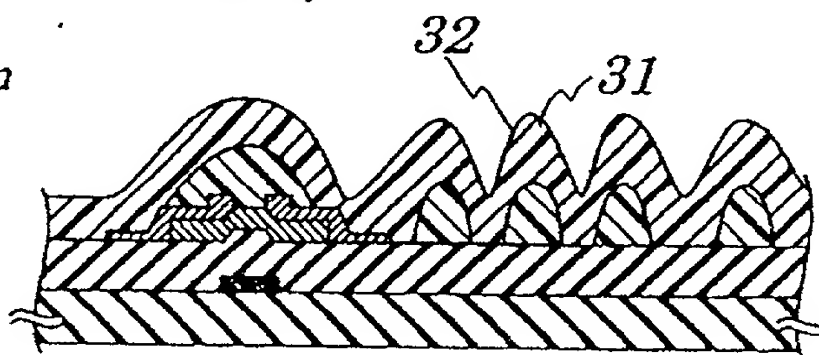


FIG.21I (PRIOR ART)
forming contact region

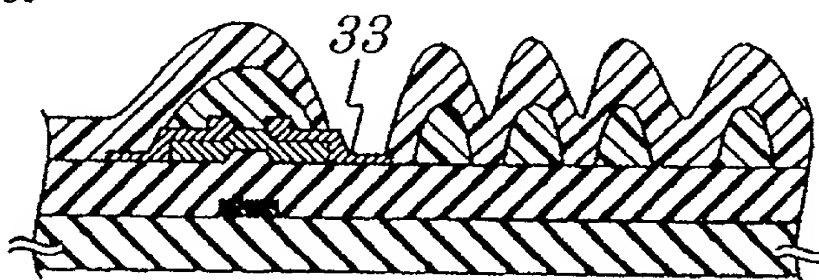


FIG.21J (PRIOR ART)
forming reflecting pixel electrode plate

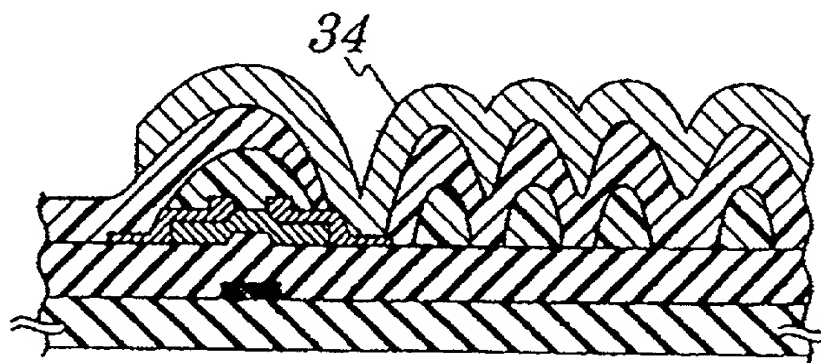


FIG.22 (PRIOR ART)

